M. Nagaraju Naik

I am very much honoured to be the chosen person to carry out the responsibilities of chief editor of Journal of Electronics Design and Technology. I am Dr. M. Nagaraju Naik working as a professor in department of Electronics and Communication Engineering in one of the leading institutions. I have completed my doctorate in 2017 in image processing field at Andhra University, Vizag, Andhra Pradesh, India, Masters in Digital Systems and Computer Systems (DSCE) from JNTU Anantapur. I am actively organizing & participating in International/National Conferences and published many Sci/Scopus/UGC indexed papers. Currently contributing in paper reviews in Springer and other conferences and have also received best faculty award from Knowledge Research Academy in 2023. The main objective of this journal is to highlight and publish an article of very high quality, original research work, new advancements, and relevant insightful reviews. They will mainly focus on modern concepts, research, and innovative advancements in the field of digital electronics, digital design systems, embedded systems and cutting-edge technologies etc. All the articles will undergo a proper and systematic review process by a team of highly qualified and experienced experts in the relevant field. Articles will go through a proper phase of validation and screening and only high-quality content with innovative and creative aspects, theoretical concepts, and advancements in the field of electronics, digital design, Technology advancements, and electronic components will be selected for publication. A highly creative, maintained professional standards and guidelines of the profession and moral etiquettes will be considered in publishing the journal. I request all the professionals those who wish to submit individually or collaboratively among authors to publish in this esteemed journal of high quality and make a substantial contribution in the advancement of the journal to the global stage to come forward. The success of the journal depends upon the authors and readers who make collective and individual contributions by publishing and spreading the message of journal across the globe. I invite all the professionals to collectively spread the awareness and to maintain the standard of utmost quality. I thank in advance to all the writers and readers for your contribution to the "Journal of Electronics Design and Technology.